

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

Lutze et al.

Title:

Methods for Identifying Non-Volatile Memory Elements with Poor

Threshold Slope of Weak Transconductance

Application No.:

10/665,685

Filing Date:

September 17, 2003

Examiner:

Unknown

Group Art Unit:

Unknown

Docket No.:

SNDK.301US0

Conf. No.:

Unknown

Certificate of Mailing Under 37 CFR 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450 Alexandria VA 22613-1450 on 12/10/123

Signature

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, Applicant(s) call(s) the documents listed on the enclosed Form PTO-1449 to the Examiner's attention in this patent application.

Copies of the documents listed on the accompanying Form PTO-1449 are enclosed.

Citation of these documents shall not be construed as (1) an admission that the documents are prior art with respect to the invention or inventions claimed in this application, (2) a representation that a search has been made (other than as indicated by any cited document), or (3) an admission that the cited information is, or is considered to be, material to patentability as defined in § 1.56(b).

SNDK.301US0

Serial No.: 10/665,685

This information disclosure statement is submitted under 37 C.F.R. § 1.97(b) and consequently no fee should be required. The Commissioner is authorized, however, to charge any fee that may be required, or to credit any overpayment, against Deposit Account No. 502664. This form is being submitted in duplicate.

Respectfully submitted,

Gerald P. Parsons Reg. No. 24,486 December 10, 2003

Date

PARSONS HSUE & DE RUNTZ LLP 655 Montgomery Street, Suite 1800 San Francisco, CA 94111

(415) 318-1160 (415) 693-0194 (fax)

Sheet 1 of 1

U.S. Department of Commerce, Patent and Trademark			Atty. Docket No.			Application No.		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT			SNDK.301US0			10/665,685		
			Applicant(s)			Conf. No.		
(Use several sheets if necessary)			Lutze et al.			Unknown		
1 ma E			Filing Date			Group		
			September 17, 2003			Unknown		
*Examiner Document Filing Document								
*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate		
						ļ		
•								
		1						
		 						
		1						
		-	<u> </u>		· · · · · · · · · · · · · · · · · · ·			
<u> </u>		I C Dublished Dat	and Amplication Door					
*Examiner Document Filing Date								
Initial	Number	Date	Name	Class	Subclass	If Appropriate		
					·			
		Foreign I	Patent Documents					
						Translation		
	Document	Date	Country	Class	Subclass	Yes	No	
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
Park et al., "Degradation of Thin Tunnel Gate Oxide Under Constant Fowler-Nordheim Current Stress for a Flash Eeprom," <i>IEEE Transactions on Electron Devices</i> , Vol. 45, No. 6, June 1998, pp. 1361-1368.								
\$	EEPROMs with I	Lee et al., "Endurance Characteristics and Degradation Mechanism of Polysilicon Thin Film Transistor EEPROMs with Electron Cyclotron Resonance N ₂ O-Plasma Gate Oxide," <i>Jpn. J. Appl. Phys.</i> , Vol. 38, 1999. pp. 2215-2218.						
Examiner Date Consider		Date Considered	:d					
*EXAMINER: citation if not in	Initial if reference conside conformance and not cons	red, whether or not idered. Include co	t citation is in conformation of this form with yo	ance with MP	EP 609; Draw ation to application	line through ant.		